Reply Under 37 C.F.R. § 1.116 Expedited Procedure Technology Center 2100

Application No.: 10/582,118

Art Unit: 2185

LIST OF CURRENT CLAIMS

Claim 1. (Currently Amended) A smart card chip, comprising a nonvolatile system memory, a Java Card Virtual Machine implemented in the nonvolatile system memory, a nonvolatile application memory, a volatile working memory and a variables memory area reserved for global variables, the variables memory area being reserved in the volatile working memory;

wherein the variables memory area is reserved by a Java package implemented in the smart card chip;

wherein the variables memory area is accessible only by programs stored in the system memory;

wherein said programs may access the variables memory area that can link to the variables memory area; and

wherein an export component of the Java package containing the link information required for linking to the reserved variables memory area is not implemented in the smart card chip.

Claim 2. (Canceled)

Claim 3. (Previously Presented) The smart card chip according to claim 1, wherein the variables memory area is accessible only by programs that have been implemented in the smart card chip up to the end of completion of the smart card chip.

Claim 4. (Previously Presented) The smart card chip according to claim 1, wherein the variables memory area is reserved statically.

Reply Under 37 C.F.R. § 1.116 Expedited Procedure Technology Center 2100

Application No.: 10/582,118

Art Unit: 2185

Claim 5. (Previously Presented) The smart card chip according to claim 1, wherein the variables memory area is reserved by access information directed directly to the working memory.

Claim 6. (Canceled)

Claim 7. (Canceled)

Claim 8. (Previously Presented) The smart card chip according to claim 2, wherein said programs are excluded from using the variables memory area that cannot link to the variables memory area.

Claim 9. (Previously Presented) The smart card chip according to claim 8, including a program having withheld therefrom for linking an export component of the smart card chip to prevent the program to link to the variables memory area.

Claim 10. (Canceled)

Claim 11. (Previously Presented) The smart card chip according to claim 1, wherein the Java package is implemented in the system memory.

Claim 12. (Previously Presented) The smart card chip according to claim 1, wherein the Java package contains exclusively the reservation of the variables memory area.

Claim 13. (Canceled)

Claim 14. (Previously Presented) The smart card chip according to claim 1, wherein the Java Card Virtual Machine is so formed, and modified when required,

Reply Under 37 C.F.R. § 1.116 Expedited Procedure Technology Center 2100

Application No.: 10/582,118

Art Unit: 2185

that it enables the use of global variables in the volatile working memory on the basis of the variables memory area reserved in RAM.

Claim 15. (Previously Presented) The smart card chip according to claim 14, wherein the Java Card Virtual Machine is modified such that the modifications are not qualitatively recognizable externally.

Claim 16. (Previously Presented) A chip module having a smart card chip according to claim 1.

Claim 17. (Previously Presented) A data carrier having at least one of a smart card chip according to claim 1 and a chip module according to claim 16.

Claim 18. (Canceled)

Claim 19. (Canceled)

Claim 20. (Canceled)

Claim 21. (Canceled)